

Seventeenth Annual

Semiconductor Thermal Measurement and Management Symposium and Exposition

March 20-22, 2001 – San Jose, California DoubleTree Hotel San Jose, California

SESSIONS

Tuesday, March 20

- Simulation in Thermal Management
 Novel Measurement Techniques
- ► Poster Session

Wednesday, March 21

- Advances in Compact Models
 Active Cooling Technology
- Thursday, March 22
- Thermal Enhancement Technology
 Component Thermal Performance



The Institute of Electrical and Electronic Engineers, Inc.



IEEE Components, Packaging and Manufacturing Technology Society



The National Institute of Standards and Technology

EVENING WORKSHOP Tuesday, March 20, 7:30 – 9:00 pm.

Panel: Optoelectronic Packaging–Thermal Aspects and Challenges PANEL CHAIR: Jeff Montgomery, Electronicast

Significant Contributor Thermi Award Presentation

Thursday, March 22, 8:15 - 9:00 am RECIPIENT: Clemens J. M. Lasance, Philips Research "Capita Selecta in Electronics Cooling"

OPTIONAL 2-DAY SHORT COURSE (In Advance of Regular Sessions) Sunday & Monday, March 18-19, 2001, 9 am – 5 pm

> Thermal Design for Electronics Cooling Applications Robert E. Simons, IBM

> > Register On-line: www.semi-therm.org

EXHIBITS & WORKSHOPS FREE ATTENDANCE

For Exhibitor Packet, Call 520-323-2870, Fax 480-345-1119, or e-mail: cscom@goodnet.com

SEMI-THERM provides attendees the opportunity to view the latest equipment and services related to thermal management and measurement on the afternoons of March 20 and 21. Vendor workshops provide either basic technical information or specific applications information regarding an exhibiting company's product.

(Partial List as of January 12, 2001)

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Exhibits, Workshops & Tuesday Evening Workshop FREE of Charge to Attend! Register for an EXHIBITS-ONLY BADGE Register On-line: www.semi-therm.org

SHORT COURSE

Thermal Design for Electronics Cooling Applications

Sunday-Monday, March 18-19

Instructor: Robert E. Simons, IBM

TELEPHONE: 845-433-5262 • FAX: 845-432-9805 • E-MAIL: resimons@us.ibm.com

Introduction

This intensive short course is designed to provide an appreciation and understanding of the important role of thermal design in the development and operation of electronic equipment, as well as the basic techniques for estimating thermal performance of electronic packages and systems. Fundamental principles of thermal conduction for heat transport within an electronic package, and convection (natural and forced) for heat removal from the package are presented. Methods to estimate heat transfer coefficients and thermal spreading resistance are covered, as well as radiation and liquid immersion heat transfer.

The use of heat sinks to augment air cooling performance is discussed along with simple methods to predict heat sink performance. Flow and pressure drop characteristics in air-cooled systems are also covered. The use of fan performance and system flow impedance curves to determine total air flow rate and flow distribution in a system is presented. Easy-to-use methods of package cooling analysis utilizing the junction temperature equation, thermal resistance concepts, and simple network models are included.

Many annotated numerical examples are provided throughout the course to illustrate the application of basic concepts and equations to model heat flow in electronic packages from the chip level to system level.

This course will be of interest to electrical and mechanical engineers with a need to understand thermal design. The course is presented at a level which will be understandable even to those with no prior heat transfer background. The course also provides information of interest and of use to experienced thermal engineers.

Topic Outline

Introduction Objectives Power Dissipation Trend(s) Temperature and Reliability

Thermal Design Objectives Thermal Design Variables Thermal Management Options

Conduction
 Fourier's Law
 Electro-Thermal Analog
 Series and Parallel Conduction
 Thermal Spreading Resistance
 Thermal Contact Resistance

Convection

Newton's Rate Equation Heat Transfer Coefficient Relative Cooling Capability Dimensionless Groups Fluid Properties Natural Convection Heat Transfer Forced Convection Heat Transfer

Radiation

- Planck's Law Stefan-Boltzman Law Emissivity Radiation Equation Radiation Heat Transfer Coefficient
- Immersion Cooling
 Single Phase
 Pool Boiling
 Flow Boiling
- Extended Surfaces and Heat Sinks Typical Extended Surfaces Fin Efficiency Concept Straight Fin Heat Sink Analysis Radial Fin Heat Sink Analysis Effect of Air Bypass
- Air Flow and Pressure Drop
 Fan Curves and System Flow Resistance
 Pressure Drop Equation
 Electro-Flow Analog
 Flow Network Modeling
- Package Cooling Analysis
 Junction Temperature Equation
 Internal and External Resistance
 Thermal Network Modeling

Instructor

Robert Simons has been engaged in the development and application of cooling technologies for electronic equipment for more than 30 years at IBM. While at IBM he participated in the thermal design and development of cooling technologies for the IBM 3033, 3081 and 3090 Processors, as well as direct liquid immersion cooling techniques. He holds 30 cooling patents and has published many papers and articles related to cooling electronics. He has conducted many lectures and seminars on cooling electronic equipment around the world.

Cost: \$525.00

Advanced Program

TUESDAY, MARCH 20, 2001	II-3	Thermoreflectance Imaging of Superlattice Micro Refrigerators
MORNING		J. Christofferson, D. Vashaee, A. Shakouri
Symposium Opening and Welcome GENERAL CHAIR: Bruce GueninAmkor Tec Technical Session I: Simulation in Thermal Mana	chnology, Inc. <mark>agement</mark>	P. Melese
SESSION CHAIR: Bill Maltz Electronic Cool		sion III: Poster Session Preview
I-1 Characterization of Laminar Jet Impingem in Portable Computer Applications John R. GuarinoRaytheon Vincent P. MannoTu	Systems Co.	
I-2 Optimization of Thermal Resistance in Qua Monolithic Integration Technology (QMIT) M. Joodaki, G. Kompa, H. Hillmer, R. Kassing Dept. of High Frequency University of Kass	asi Structure Engineering,	Complex 3-Dimensional Systems for Device and Circuit Level Electro-Thermal CAD W. Batty, S. David, A.J. Panks, R.G. Johnson, C.M. SnowdenUniversity of Leeds, UK C.E. Christoffersen, M.B. Steer
I-3 Thermal Design of a Desktop Computer Sy CFD Analysis C. W. Yu, R. L. Webb 	III-3	North Carolina State University Revising the Goals and Means for the Base-to-Air Cooling Stage for Semiconductor Heat Removal– Experiments and Their Results
I-4 Thermal Analysis of IGBT and Hybrid Pow with the Boundary Element Method		V.S. Travkin, K. Hu, M. Rizzi, I. Catton University of California, Los Angeles Thermal Management for High Performance Integrated
Zoubir Khatir, S. LefebvreINF I-5 Dynamic Electro-Thermal Physically Based Models of the Power Devices for Device a Simulations	d Compact	Circuits with Non-Uniform Chip Power Considerations T.D. Yuan, Bor Zen Hong IBM Microelectronics Howard Chen, Li-Kong Wong IBM Research Divisior
P.M. Igic, P.A. Mawby, M.S. Towers		Thermal Comparison of Plate, Extrusion Heat Sink, and Skive Heat Sink Michael C. YangMotorola
Coffee Break	9:55-10:15 III-6	The Development of a Natural Graphite Heat-Spreader
Technical Session II: Novel Measurement Technical	iques .10:15-11:15	Julian Norley, Jim JW. Tzeng, George Getz, Jeremy Klug, Brian FedorGraftech Inc
SESSION CHAIR: Savithri Subramanyam, Texas II-1 A High-Speed Thermal Imaging System Fo Semiconductor Device Analysis		Thermal Characterization of Fan-Heat Sink Systems in Miniature Axial Fan and Micro Blower Airflow C.K. Loh, Dan Nelson, D.J. ChouEnertron, Inc
A. Hefner, D. Berning, D. Blackburn, C. Chapu S. BoucheNational Institute an	of Standards d Technology	Multi-Objective Placement Optimization of Power Electronic Devices on Liquid Cooled Heat Sinks Deepak Gopinath, Yogendra K. Joshi, Shapour Azarm
II-2 Design Issues of a Multi-Functional Intellig Thermal Test Die		CALCE Electronic Products and Systems Consortium University of Maryland
A. Poppe, G. Farkas, M. Rencz, Ys. Benedek, L. F V. Székely, K. Torki, S. Mir, B. CourtoisMicReE		

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If you are *not* currently an **IEEE** member, join now and save \$45 on SEMI-THERM registration, plus receive FREE membership in the **Components, Packaging and Manufacturing Technology Society.**

You must complete a membership application and obtain an IEEE member number BEFORE sending in registration. For details, contact: Marsha Tickman, IEEE CPMT Society, phone: 732-562-5529, e-mail: m.tickman@ieee.org



SEMI-THERM 2000: General Chair Marcelle Ibrahim honors Finance Chair Al Angevine at Awards Luncheon.



SEMI-THERM 2000: Mike Boyle and General Chair Marcelle Ibrahim present Kaveh Azar with the Significant Contributor Thermi Award.

Jukka Rantala Nokia Research Center, Finland

AFTERNOON

Exhibits Open1:30-6:00
Vendor Workshops2:00-5:15
Poster Session1:30-3:00

EVENING

Dinner	 6:15–7:15

Evening Workshop7:30–9:00

PANEL SESSION – Optoelectronic Packaging: Thermal Aspects and Challenges PANEL CHAIR: Jeff MontgomeryElectronicast

WEDNESDAY, MARCH 21, 2001

MORNING

Announcements8:00-8:15

Technical Session IV: Advances in Compact Models

IV-1 The European Project PROFIT: Prediction of Temperature Gradients Influencing the Quality of Electronic Products Clemens J.M. Lasance . . . Philips Research Laboratories, The Netherlands

- IV-2 Creating Compact Models Using Standard Spreadsheet Software Gerhard NoebauerInfineon Technologies, Germany
- IV-4 A Generic Method for Thermal Multiport Model Generation of IC Packages M. Rencz, V. SzékelyMicReD Ltd., Hungary

LUNCHEON SPEAKER

Tuesday, March 20, 2001 Thermal Management Challenges of Third Generation Telecommunication Devices

Jukka Rantala, Nokia Research Center

In telecommunication, data transfer is gaining more importance and the relational part of voice calls is decreasing. Related to this, digital convergence is taking place: telecommunication devices and data transfer devices are going to have more and more features in common.

The third generation of telecommunication products, that will be first taken into public use in Japan in 2001, will offer high data transfer rates for mobile users, making possible for example convenient internet browsing, videoconferencing, and truly mobile office with access to all data bases, to name a few applications. However, this means that in the hand held products more power consuming processors and memories are needed, the RF amplifiers will have longer ontimes, and the operation frequencies of digital circuits will increase. Simultaneously, the miniaturization of consumer telecommunication devices puts more functions to smaller volume. This brings us a relation where the dissipated heat intensities are increasing tremendously.

In thermal management this brings new challenges: previously the main task was to transfer the heat from the component, but now it will be at least as important to dissipate the heat from the system and to keep the enclosure surface cool enough for convenient and safe use. To reach the optimum solution, thermal management has to be taken into account in different areas of electronics design, with different means towards the common target.

EVENING WORKSHOP

Tuesday, March 20, 2001

Panel Session: Optoelectronic Packaging:Thermal Aspects and Challenges

PANEL CHAIR: Jeff Montgomery, Electronicast

Optical telecommunications components are the future of high-bandwidth communications. As internet traffic increases, traditional electrical signals are limited by the medium in which they travel. Using light to move information has incredible benefits in terms of speed and density, but there are drawbacks especially in packaging and thermal management of optical devices. Optical packaging is a specialty which is drawing from many other industries such as microelectronics, aerospace and medical, but new territories are emerging that require fresh thinking and new ideas.

A group of experts on optical components will discuss thermal issues and challenges associated with this vital segment of the telecom industry. The main theme is packaging with emphasis on thermal management. Panelists include participants from suppliers as well as endusers. The session is being moderated by Jeff Montgomery of Electronicast, a well known expert on the optics industry.

LUNCHEON SPEAKER

Wednesday, March 21, 2001

"The Arte of Fyshing with an Angle —in the 21st Century"

Dr. Robert J. Moffat, Stanford University/Moffat Thermosciences, Inc.

Fly fishing goes back hundreds of years, long before Dame Juliana Berners wrote the first book on the subject. The first few hundred years were all about developing gear and techniques. We've gone from fishing in the local brooks with braided horse-hair fishing lines on willow poles to flying thousands of miles to fish for the "Biggest Rainbows in the World" using hydrophobic-plastic-encased fly lines cast with boron composite fly rods designed by aero-elasticians.

The biggest change, however, has come in our husbandry of the streams: "How do we keep clean water flowing?"

We'll start with Dame Berners' description of the joys and benefits of fishing, written in 1421 and still true! She'll advise us on where and when to fish, how to braid horsehair fishing lines, and how to tie her favorite fly patterns.

I'll demonstrate the equipment worn by the modern "well-dressed fisherman," show some still-pictures and videos of the fish we are trying to catch, and talk a bit about the technical and political problems involved in re-building our sport fisheries.

Technical Session V: Active Cooling Technology

SESSION CHAIR: Alan ClaassenIBM V-1 An Electrokinetic Closed-Loop Micro Cooler for High-

Power VLSI Chips L. Jiang, J. Koo, S. Zeng, L. Zhang, S. Banerjee, P. Zhou, J. Santiago, T. Kenny, K. Goodson Stanford University

- V-2 Current and Future Miniature Refrigeration Cooling Technologies for High Power Microelectronics Patrick E. PhelanArizona State University Victor Chiriac, Tien-Yu Tom LeeMotorola, Inc.
- V-3 Microprocessor-Based Adaptive Thermal Control for an Air-Cooled Computer CPU Module Carin Lundquist, Van P. Carey

..... University of California at Berkeley

V-4 Tool for Fast Modelling Active Heat Sinks Piotr Dziurdzia, Andrzej KosUniversity of Mining and Metallurgy, Poland



SEMI-THERM 2000: Robert Simons, recipient of the Best Paper Award.

Panel Discussion:

unanenyes/issues
Schmidt
J. M. Lasance Philips Research
Electronic Cooling Solutions
: TBA

AFTERNOON

Dr. Robert J. Moffat	Stanford University
	Moffat Thermosciences, Inc.
Exhibits Open	1:30–6:00
Vendor Workshops	
Vendor Reception	

THURSDAY, MARCH 22, 2001

MORNING

Anno	ouncements
Signi	ficant Contributor Thermi Award Presentation8:15–9:00
	Capita Selecta in Electronics Cooling Clemens J. M. Lasance
T I	· · · · · · · · · · · · · · · · · · ·
rech	nical Session VI: Thermal Enhancement Technology
VI 4	Design Considerations of High Heat Flux
VI-1	Heat Pipe Evaporators
	Jon Zuo
VI-2	Biporous Heat Pipes for High Power
V I-Z	Electronic Device Cooling
	Jinliang Wang, Ivan Catton
VI-3	The Study of Micro-Fins Heat Sink for Electronic
	Cooling Application
	Heng-Chien Chien, Chih-Yao Wang, Ming-His Tseng,
	Chun-Hsun Chu
	Industrial Technology Research Institute, Taiwan
Coffe	e Break
rech	nical Session VII: Component Thermal Performance
	Session Chair: Paul HundtTexas Instruments
VII-1	Effect of Flag Design on Thermal Performance of
•	PBGA Packages
	Bennett Joiner
VII-2	Linear Models for Temperature and Power
	Dependence of Thermal Resistance in Si, InP and
	GaAs Substrate Devices
	David J. Walkey, Tom J. Smy, Michael Maliepaard,
	Tom MacElweeCarleton University, Canada
VII-3	Analysis of Manifold Fluid Flow Networks for Cooling
	Air and Liquid Flow-Through Modular Electronics
	Scott T. JohnsonRaytheon Sensors
	and Electronics Systems
VII-4	Temperature Sensors Placement Strategy for Fault
	Diagnosis in Integrated Circuits Piotr Bratek, Andrzej KosInstitute of Electronics,
	University of Mining and Metallurgy, Poland
VII =	Wiring Statistics and Printed Wiring Board
v II-3	Thermal Conductivity
	Richard D. Nelson

SEMITTERM

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dation, such as a sign language inte Communications, Inc. Requests sho possible to allow time to arrange the a I have the following special needs/requir	uld be made as early as accommodation.
I prefer vegetarian meals.	
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Room Rate: (single or double): \$165 per night plus 10.06% tax. All rooms are Run-of-the-House and based on availability at time of check in. After **February 28, 2001**, rooms and rate subject to availability. Specify **SEMI-THERM** to receive this special rate.

Check in time 3 p.m., check out time 12 noon

Room deposit: \$165 (single or double) plus 10.06% will guarantee reservations.

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